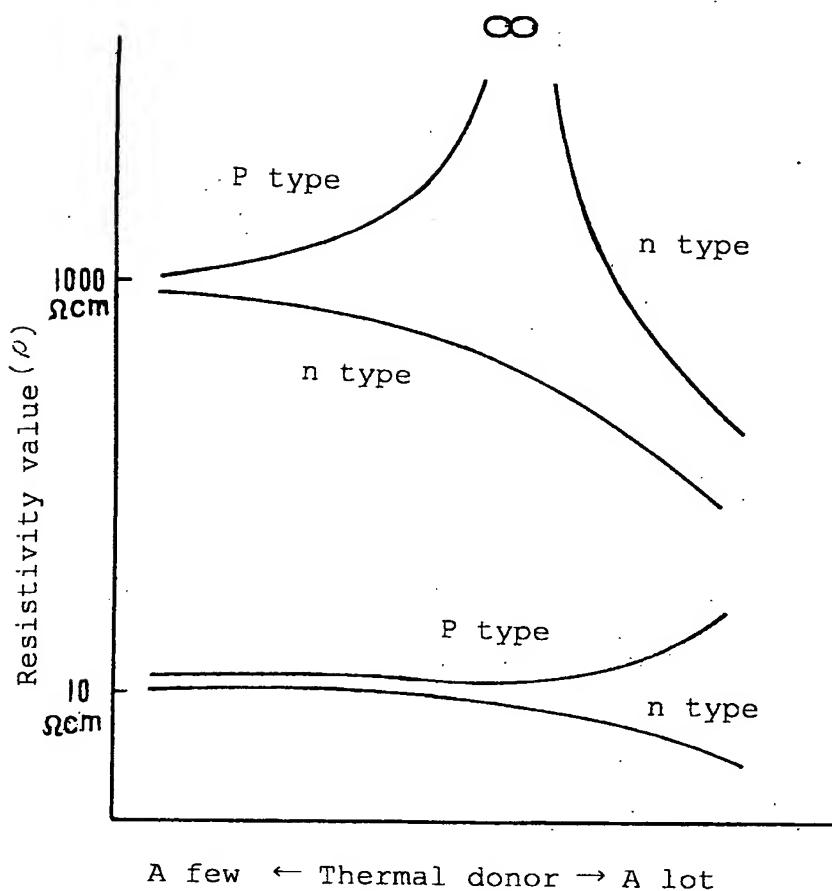


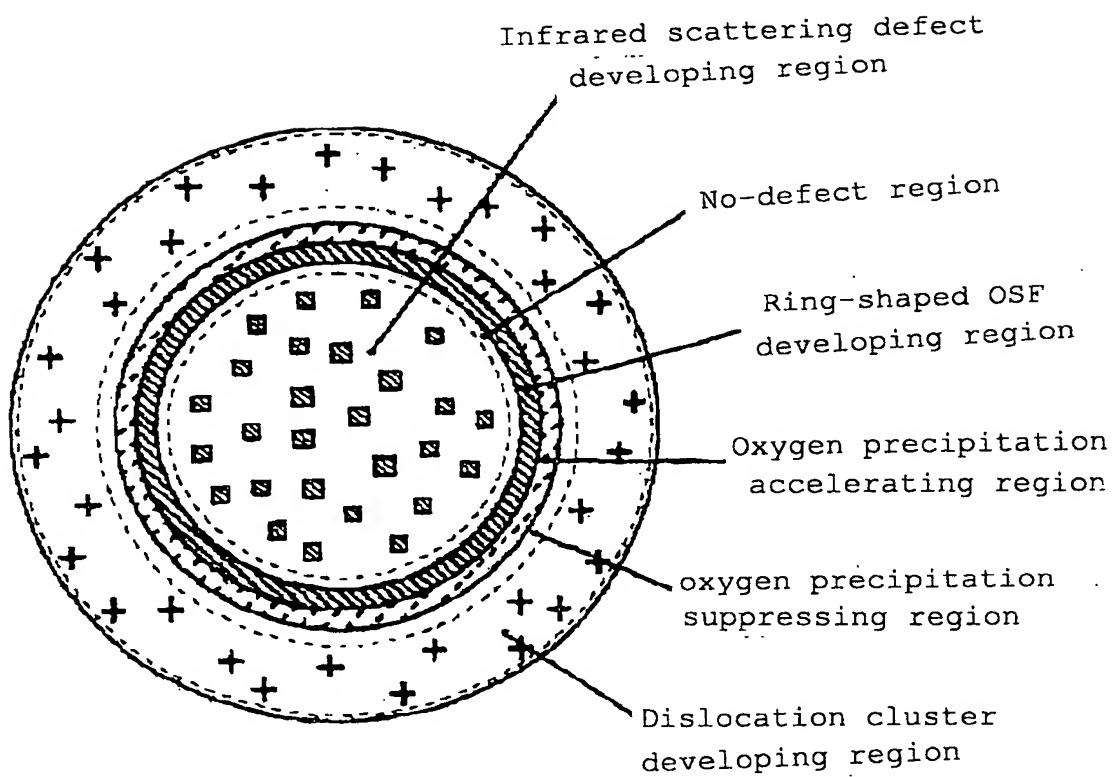
10/512405

Fig. 1



10/512405

Fig. 2



10/512405

Fig. 3

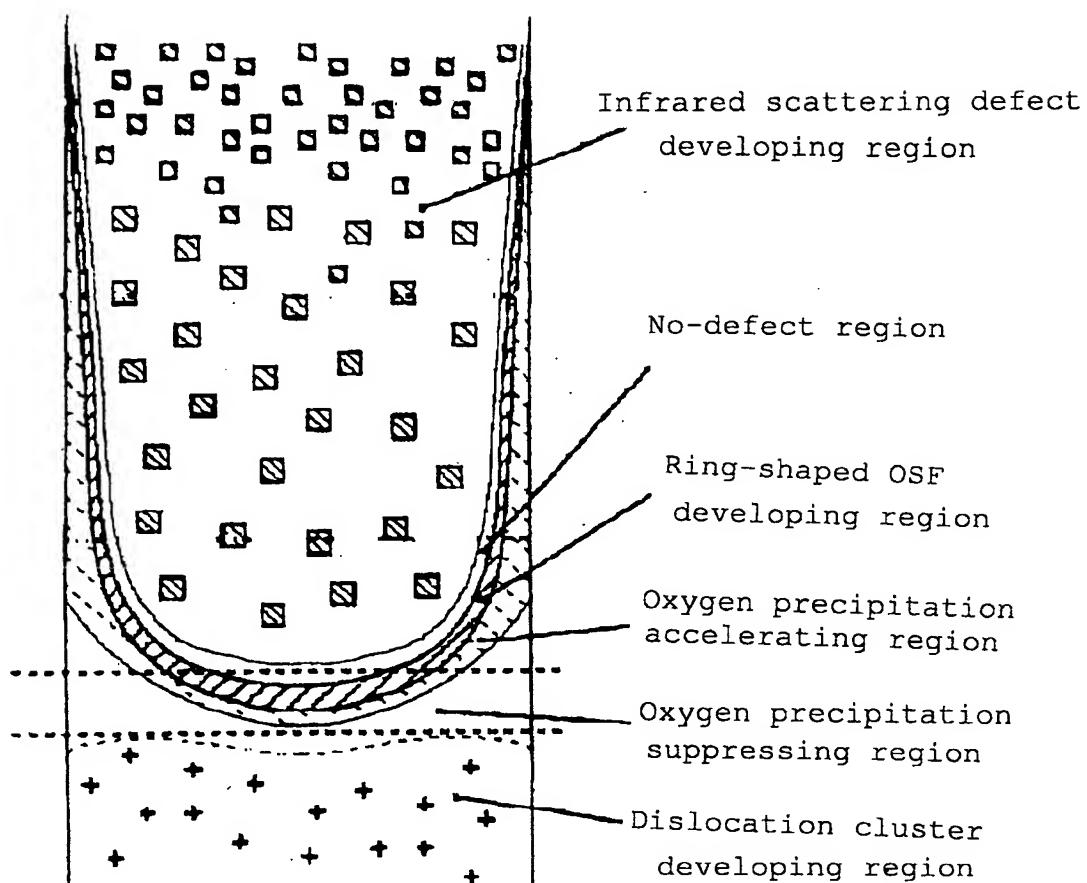


Fig. 4

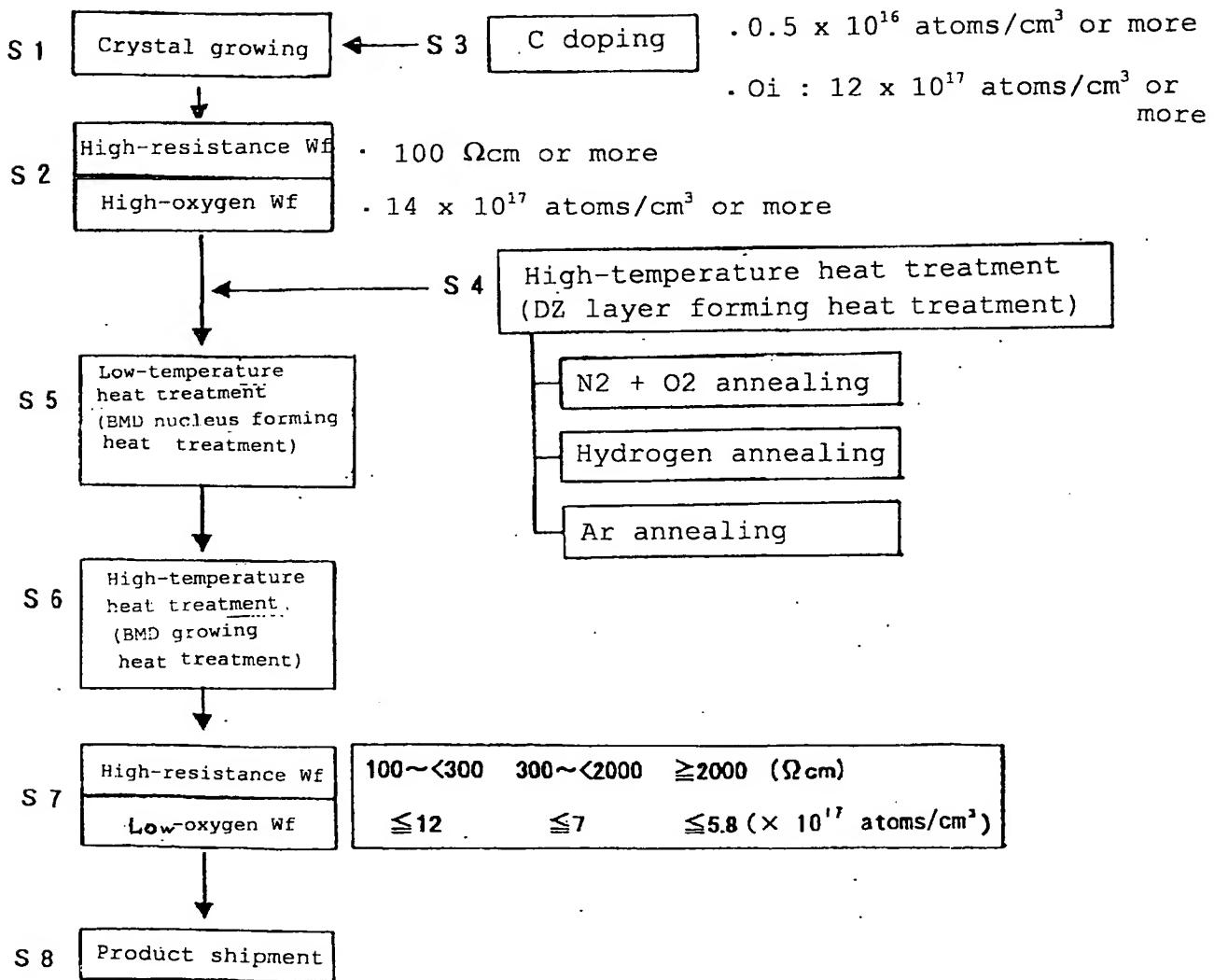
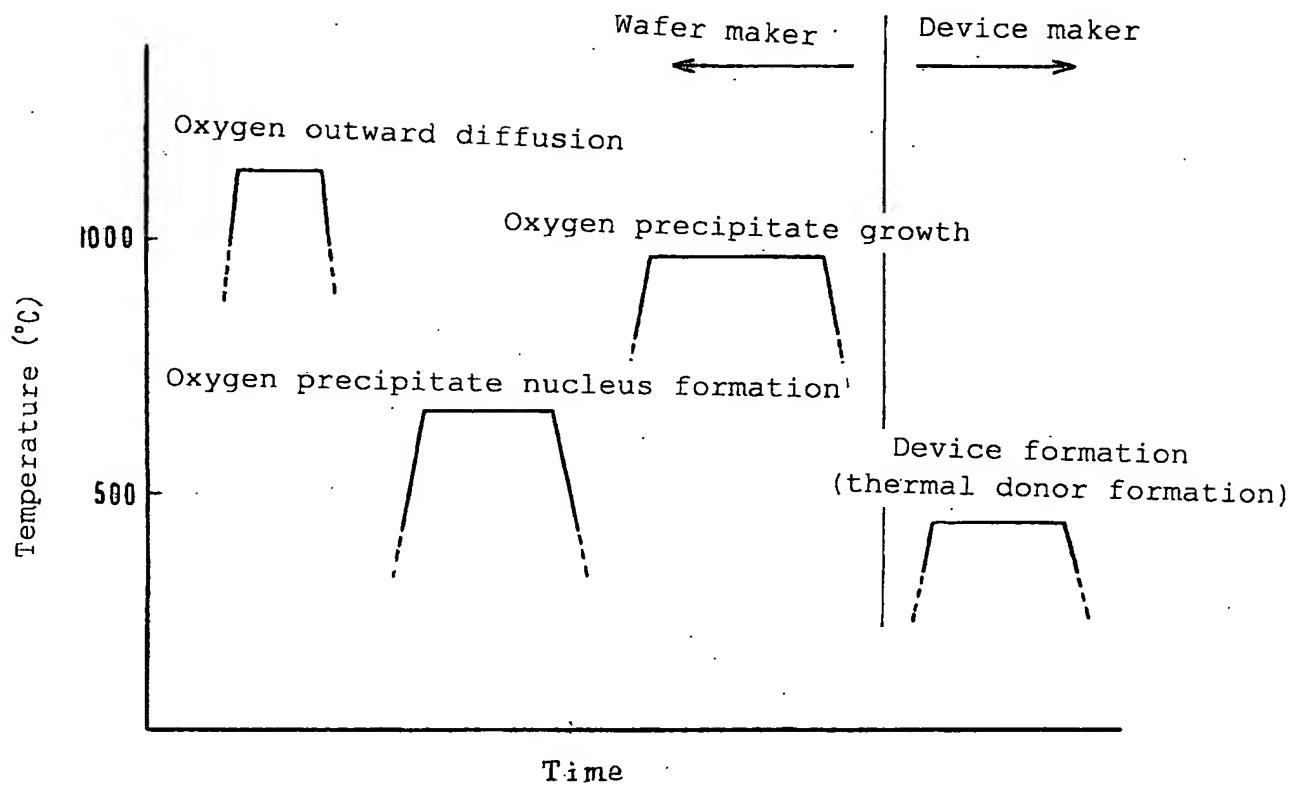


Fig. 5



10/512405

Appn. No.: New U.S. Appln. Docket No.: 474082002800
Inventor: Nobumitsu TAKASE et al.
Title: HIGH-RESISTANCE SILICON WAFER AND ITS
MANUFACTURING METHOD
Sheet 6 of 6

Fig. 6

